

FIG. 1 (PRIOR ART)





*Glitch Free Reset Circuit*  
Inventors: Sarathy Sribhashyam, et al.  
Serial No.: 09/944,963

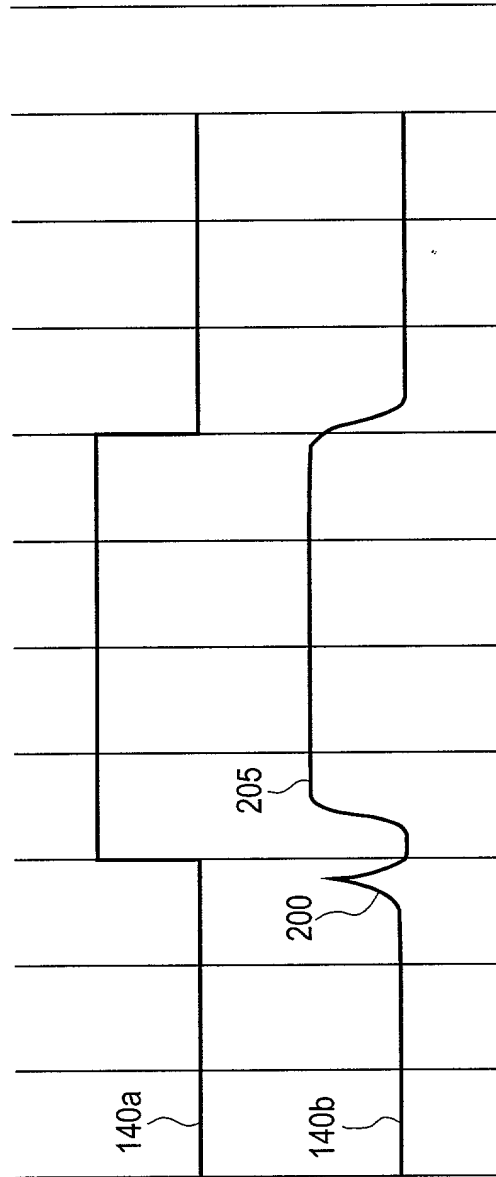


FIG. 2

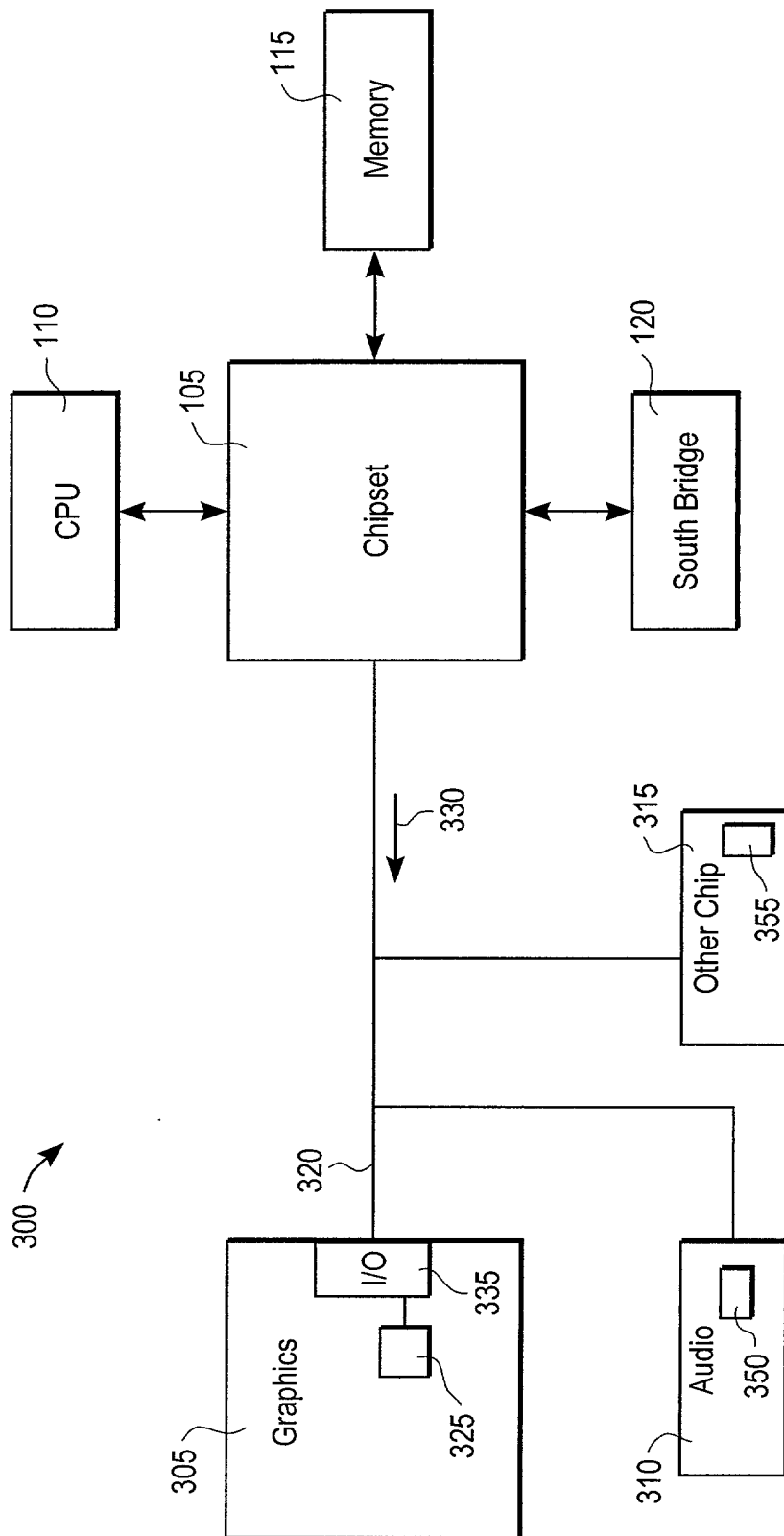
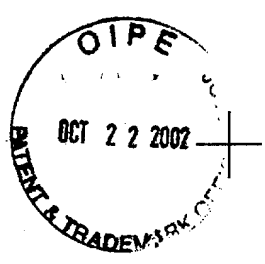


FIG. 3



202201-29644663

325a

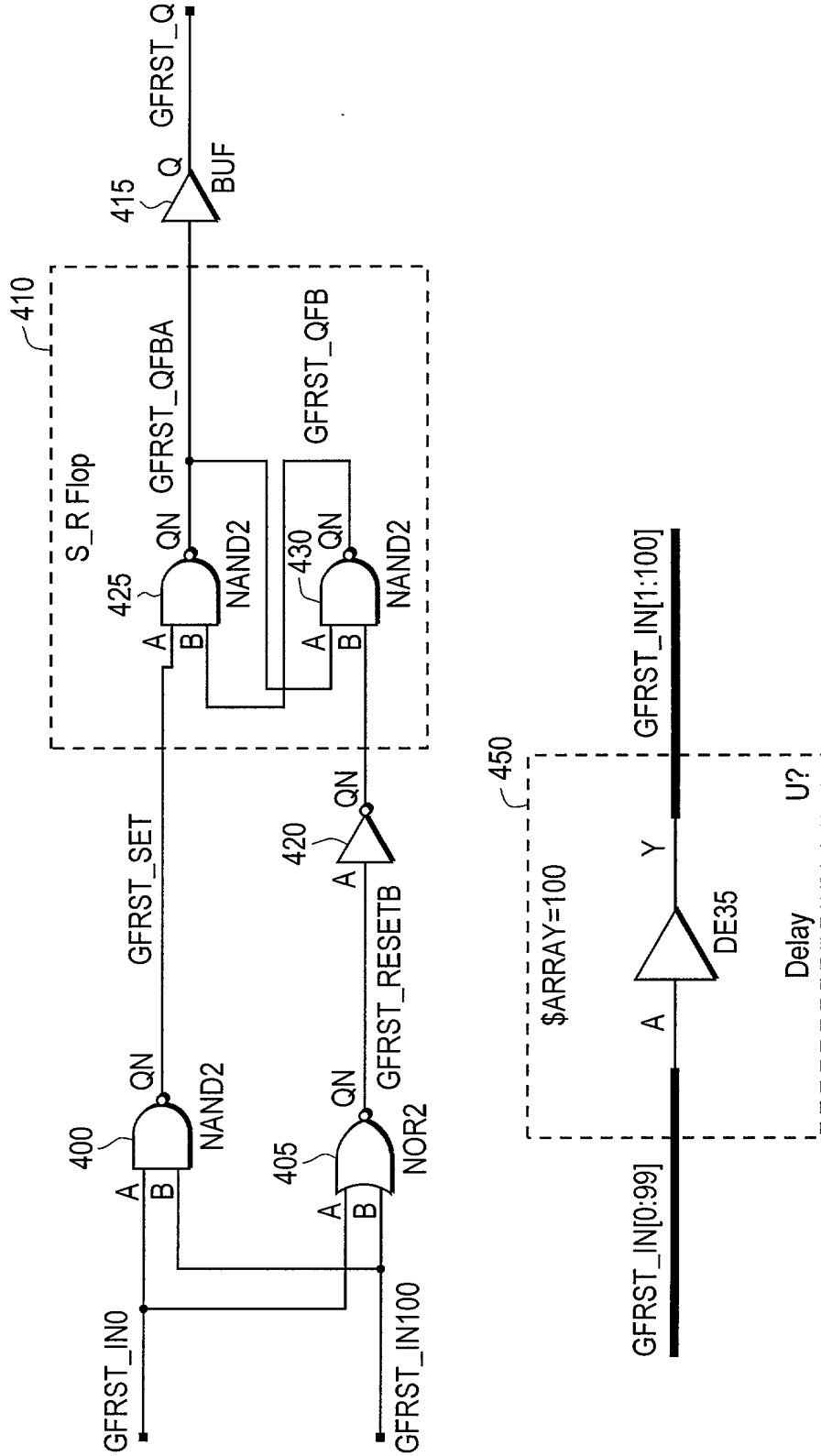


FIG. 4



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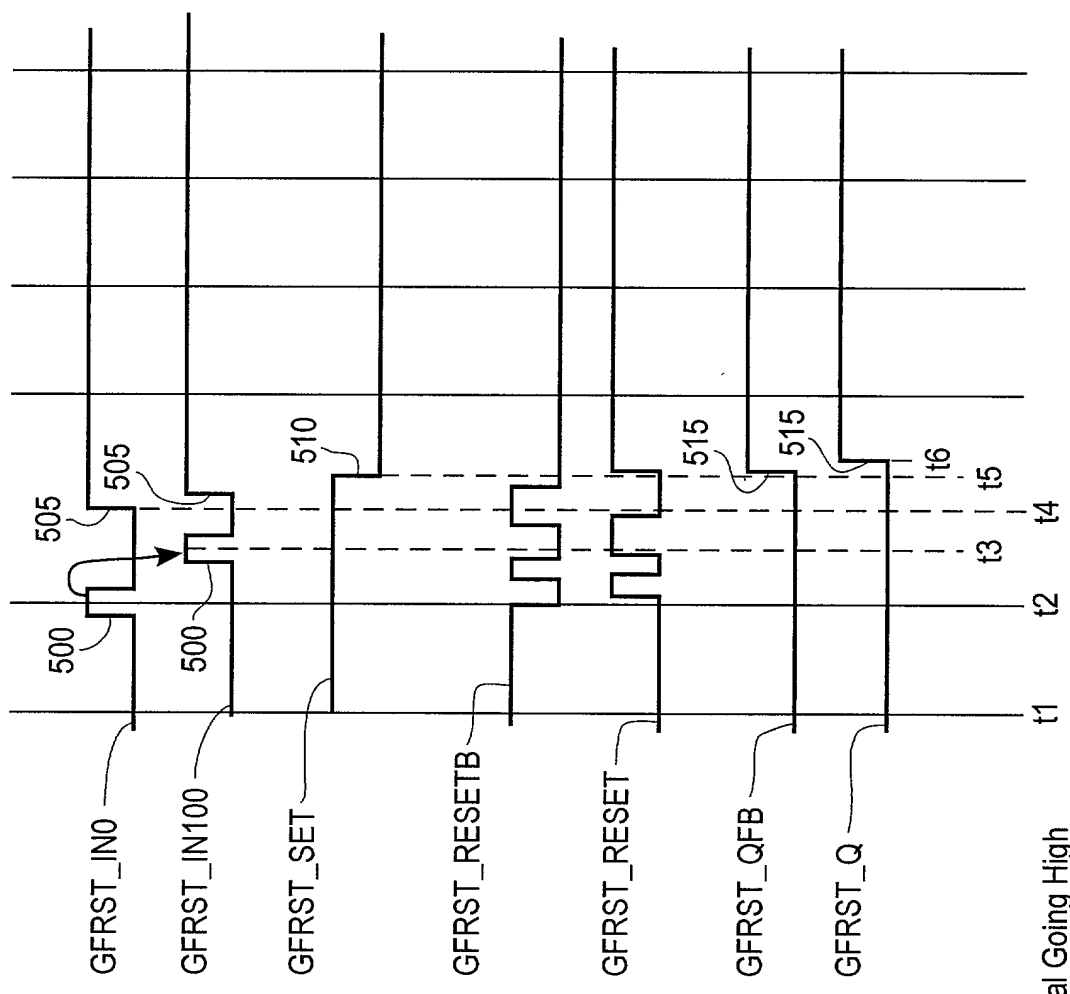


FIG. 5 Reset Signal Going High





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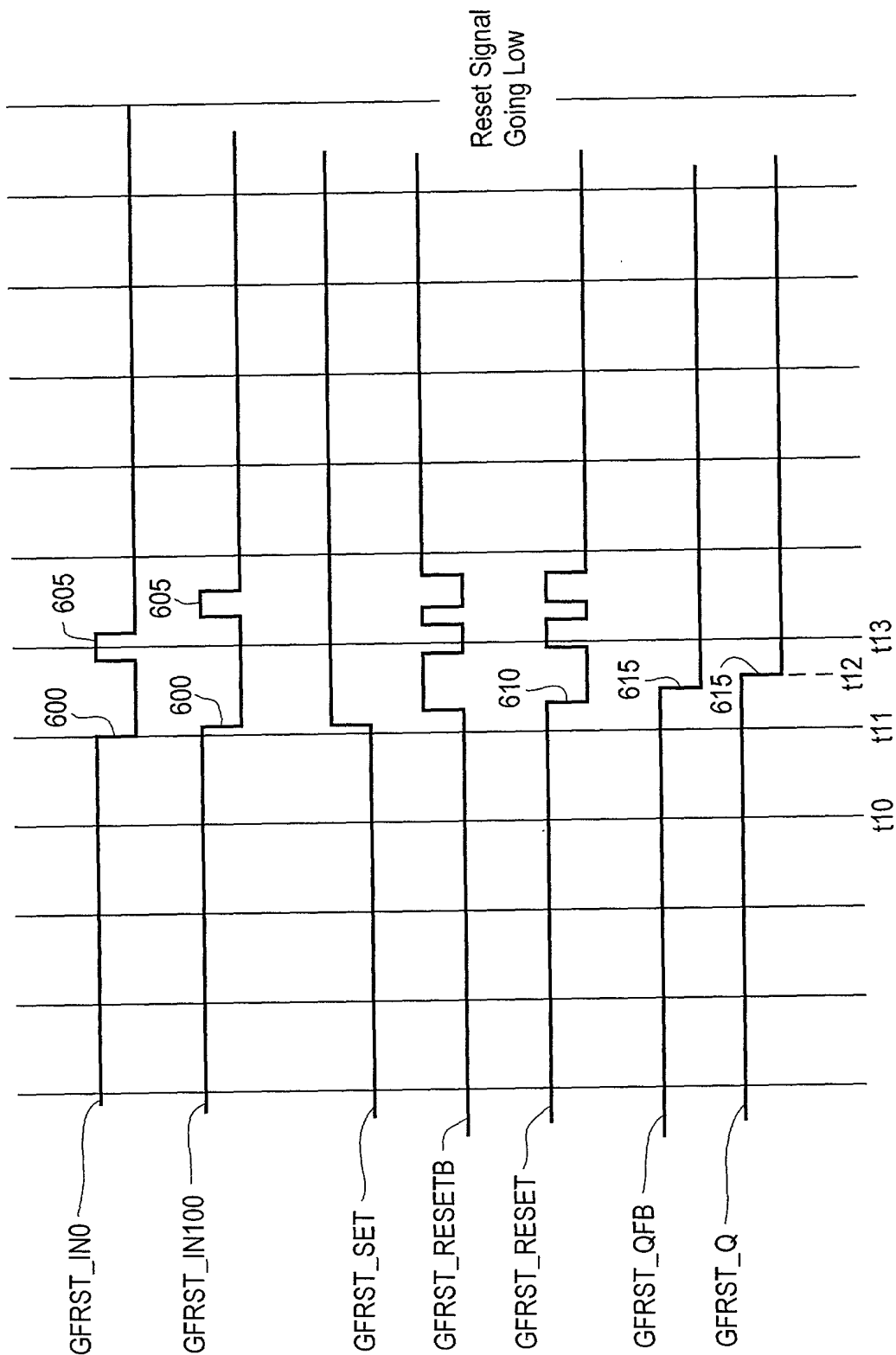


FIG. 6

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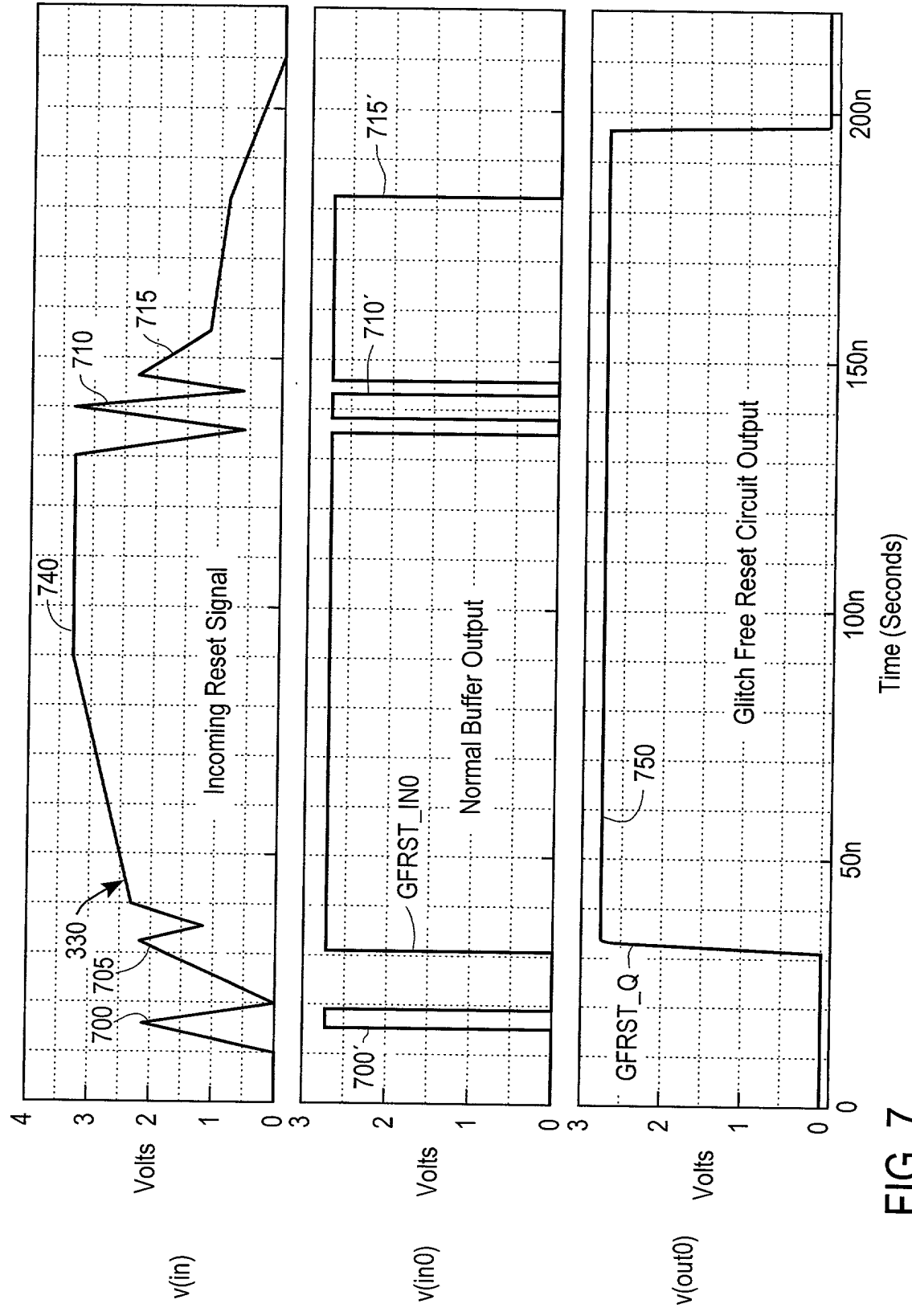
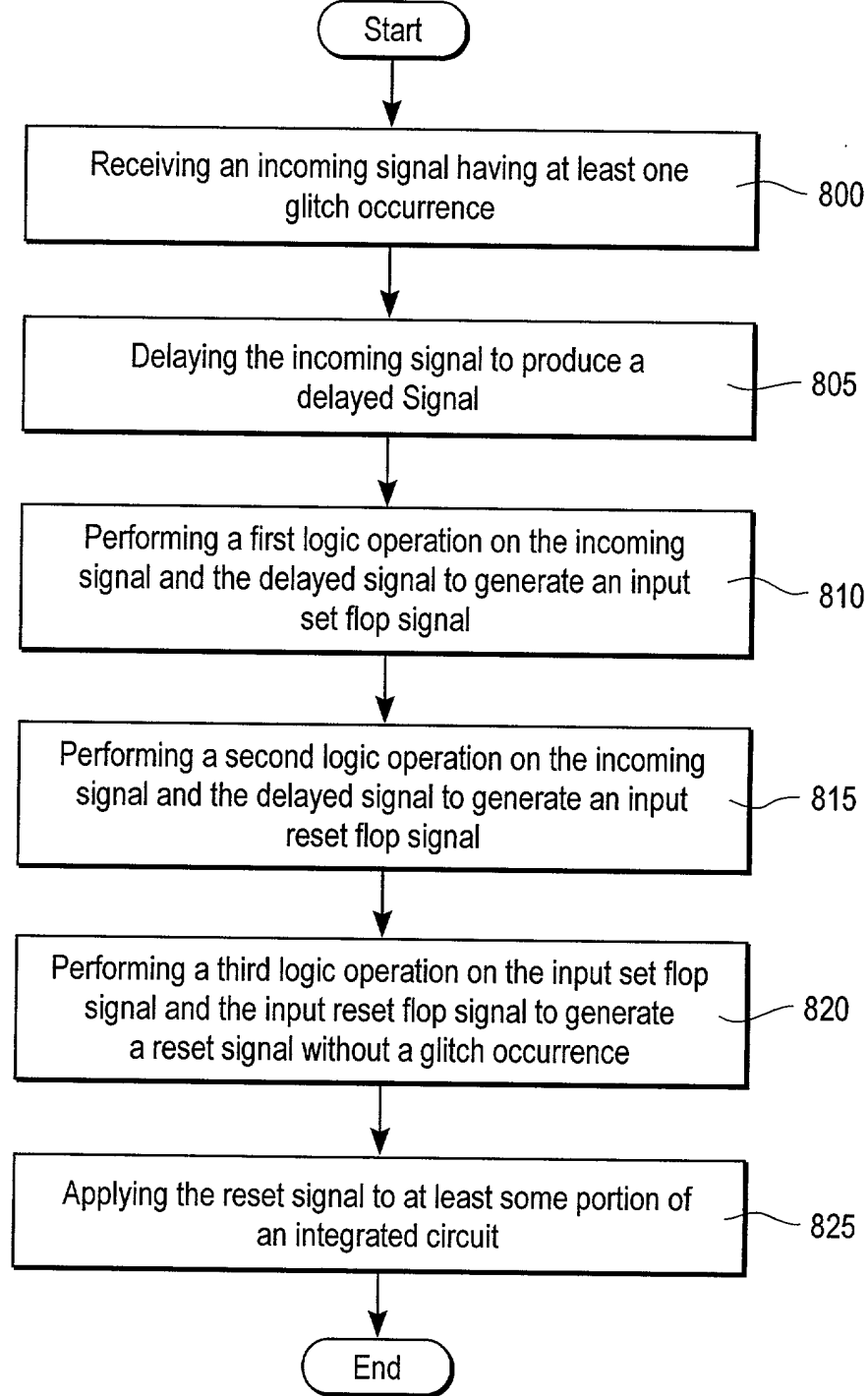


FIG. 7





Method of Compensating for Glitch Occurrence

**FIG. 8**



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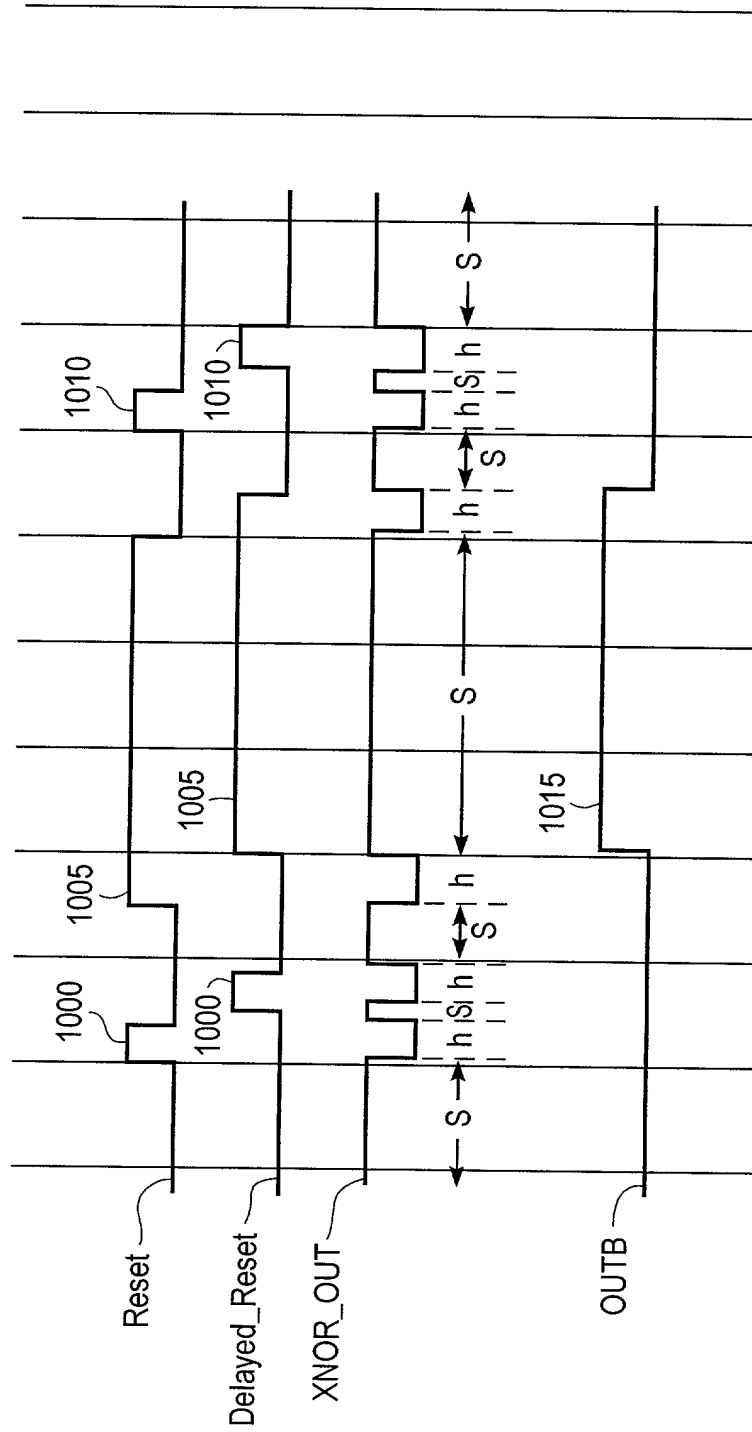


FIG. 10



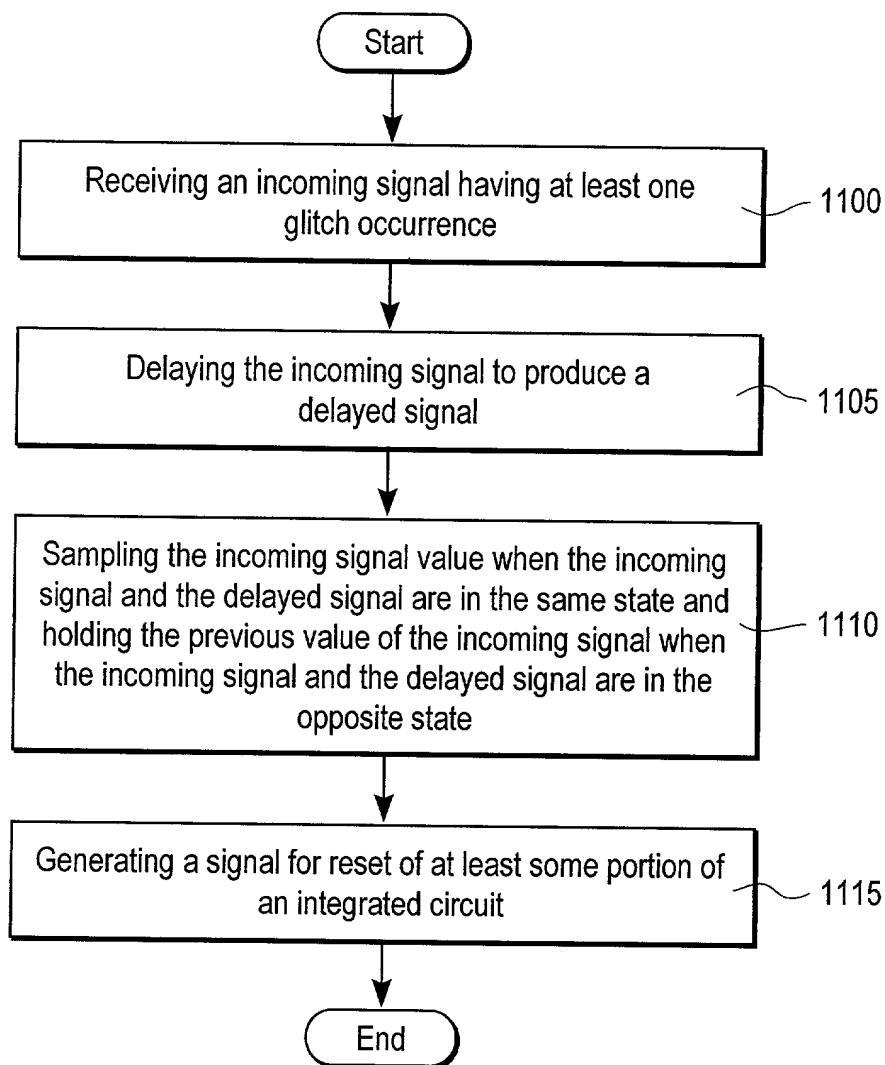


FIG. 11

